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PATENT
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In the United States Patent and Trademark Office

Honorable Commissioner of Patents and Trademarks
Washington, DC 20231

Sir:

Please file the enclosed patent application papers:

Applicant: Baird et al.

Title: Debug and Video Queue for Multi-Processor Chip

- ☒ Specification, claims, and abstract: Number of sheets 33
- ☒ Declaration/Oath: Date Signed 10/23/96
- ☒ Drawings: Number of sheets (in triplicate) 7
- ☒ Small Entity Declaration of Assignee.
- ☒ Check for \$ 385 for small entity filing fee, 37 C.F.R. § 1.16(a). Total # claims 20,
Independent Claims 3.
- ☒ Return receipt postcard addressed to applicant's Agent.
- ☒ The commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to deposit account No. **01-2950**.
☒ Any additional filing fees required under 37 C.F.R. § 1.16.
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☒ Any additional filing fees required under 37 C.F.R. § 1.16.
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Respectfully Submitted,




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I hereby certify that this paper is being deposited with the United States Postal Service using "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: "Commissioner of Patents and Trademarks, Washington, DC 20231."

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Stuart T. Auvinen, Reg. No. 36,435